

FIG. 1

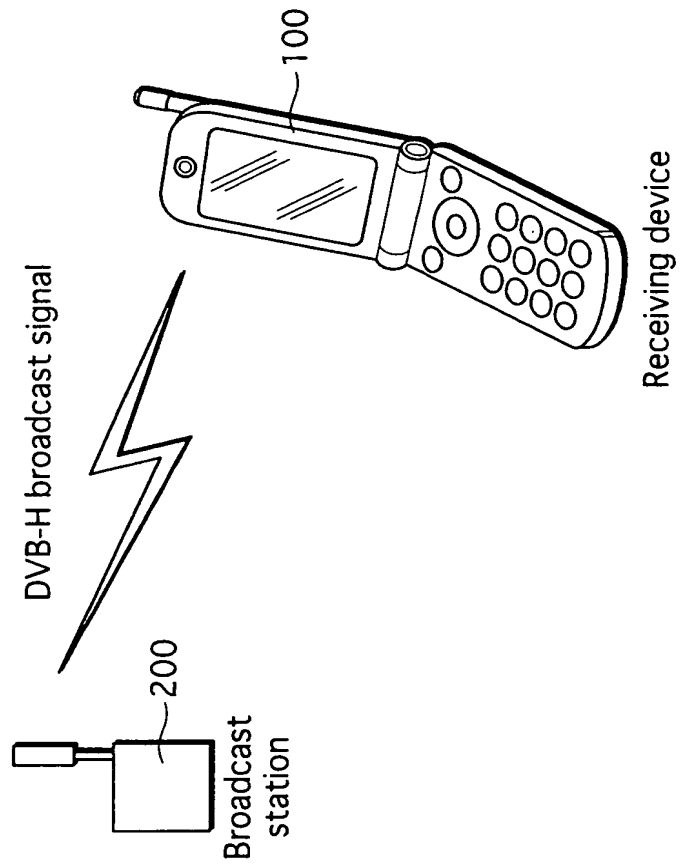


FIG. 2

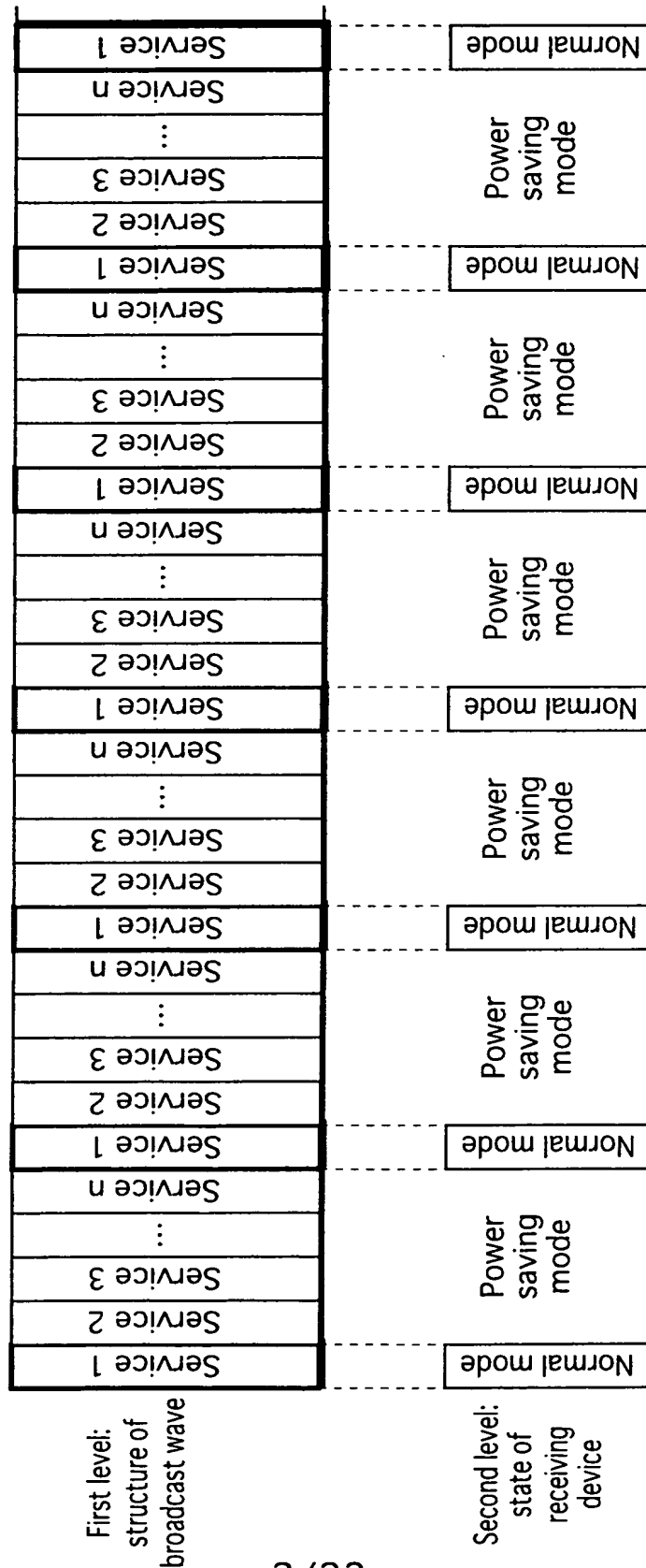


FIG. 3

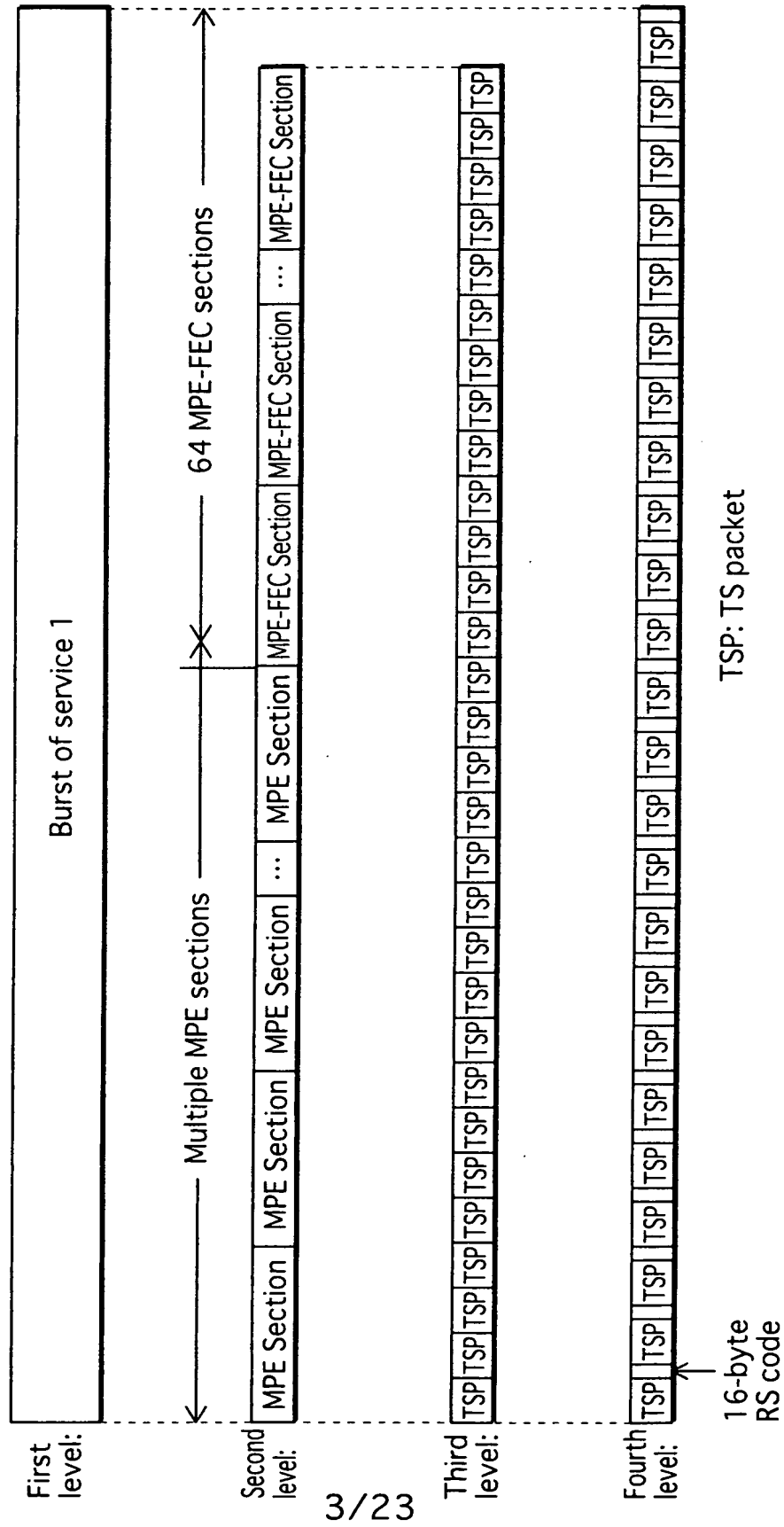
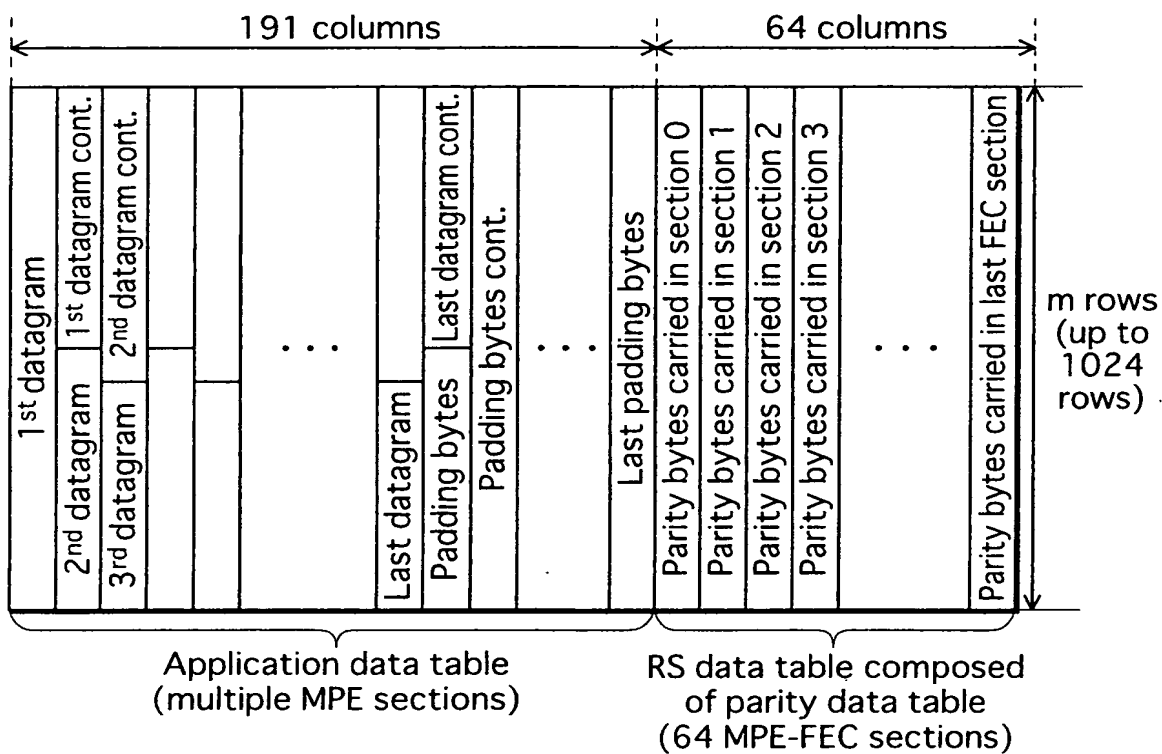


FIG. 4



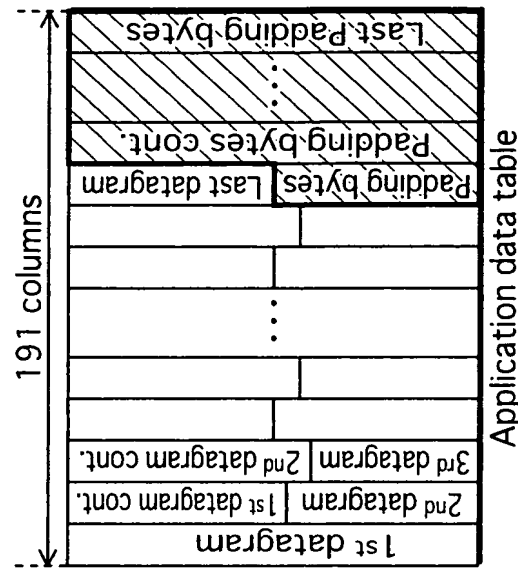
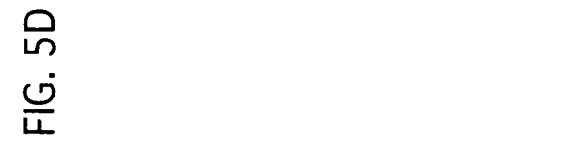
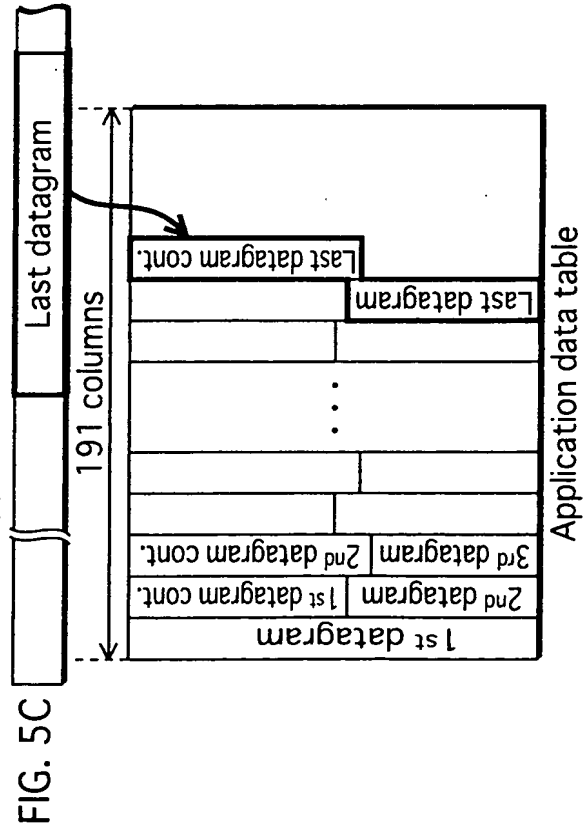
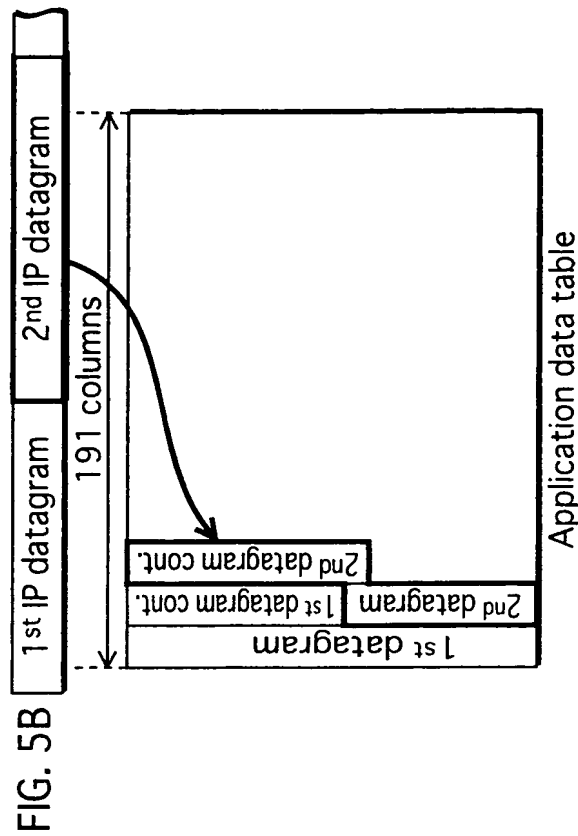
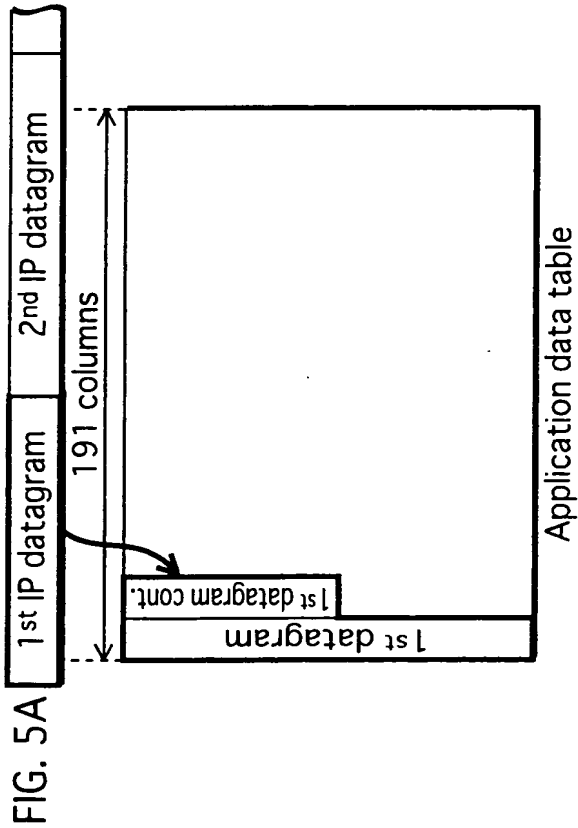


FIG. 6A

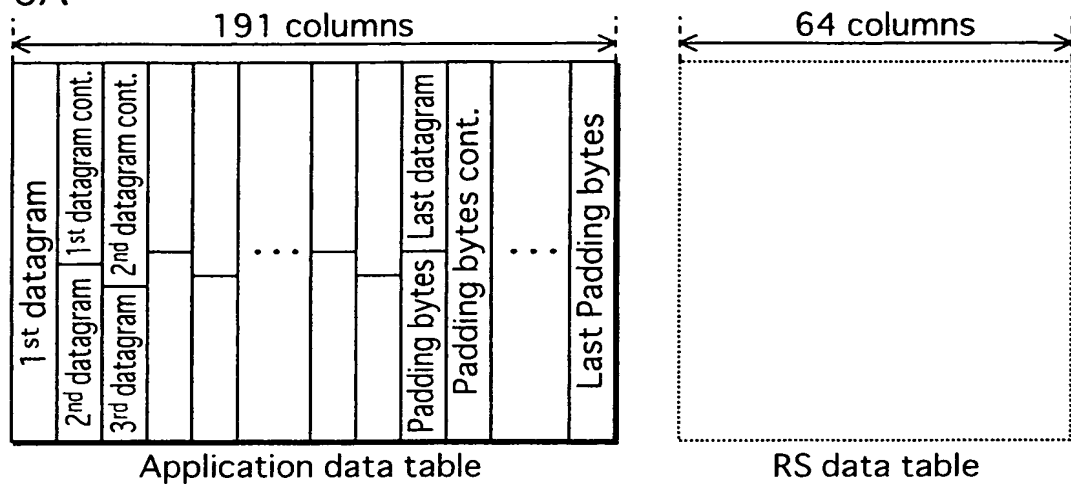


FIG. 6B

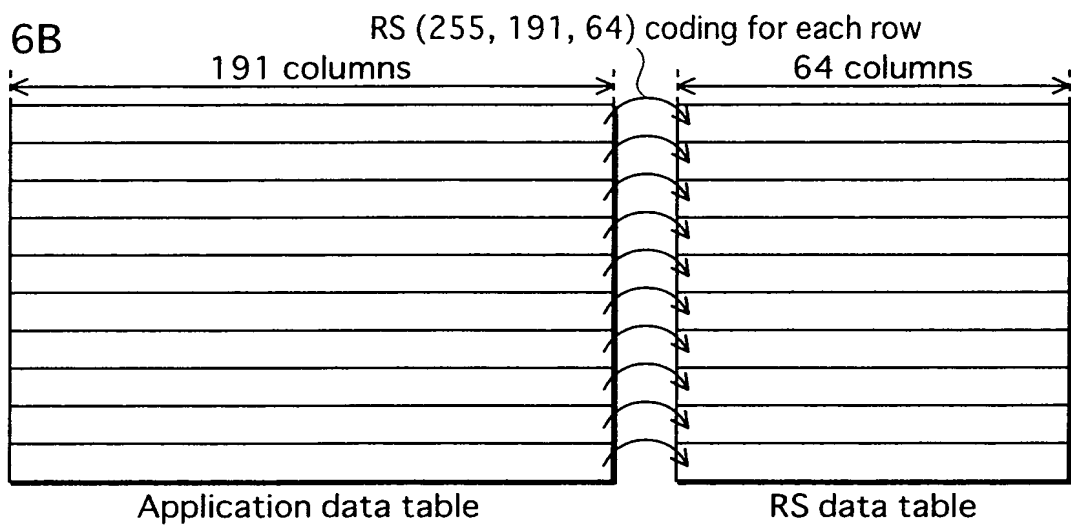
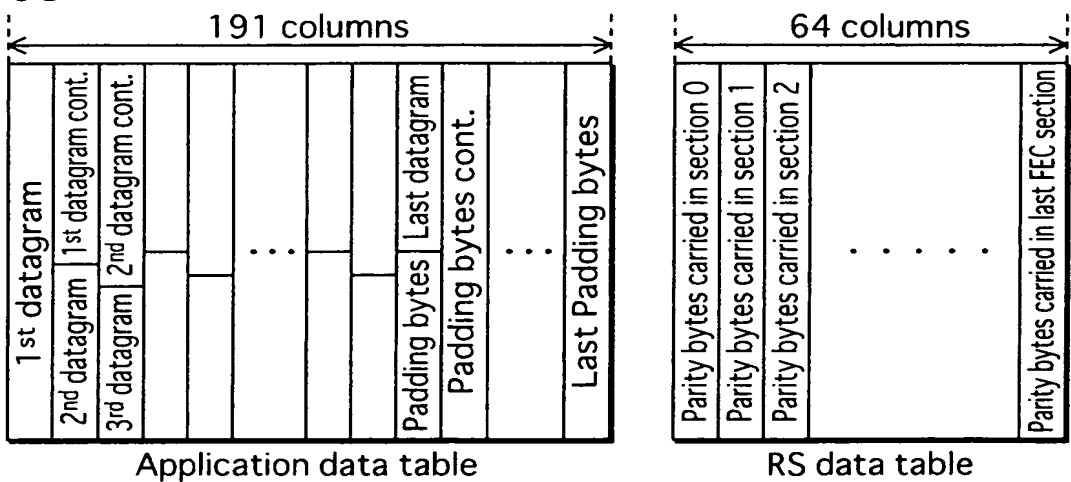


FIG. 6C



First level:

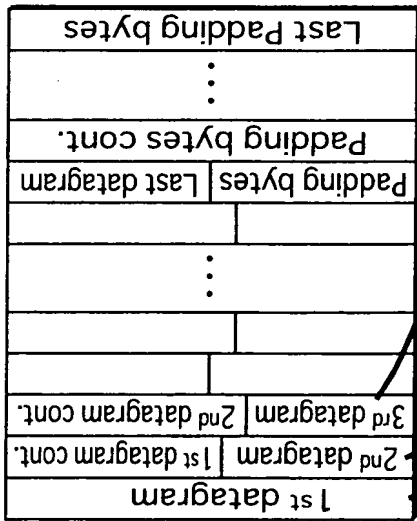
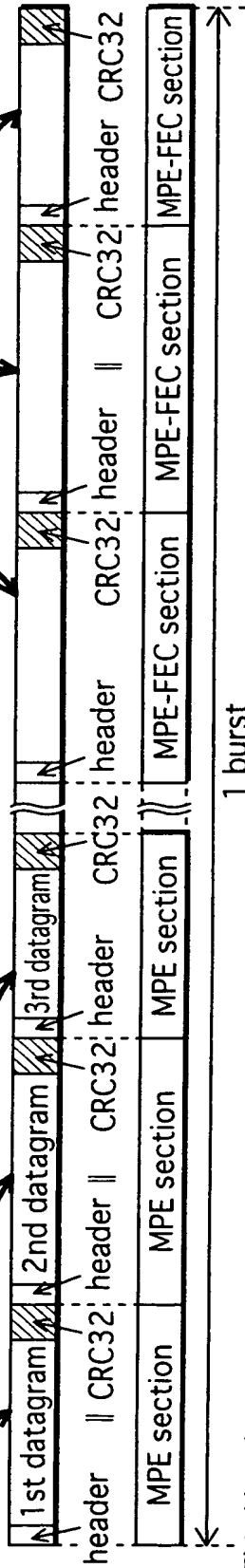


FIG. 7

Second level:

coding with header and CRC-32



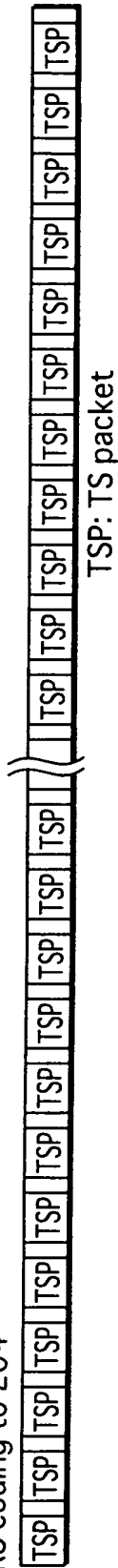
Third level: TS packetization

* convert sections into TS packets (188 bytes)



Fourth level: RS (204, 188) coding

* RS coding to 204



TSP: TS packet

FIG. 8

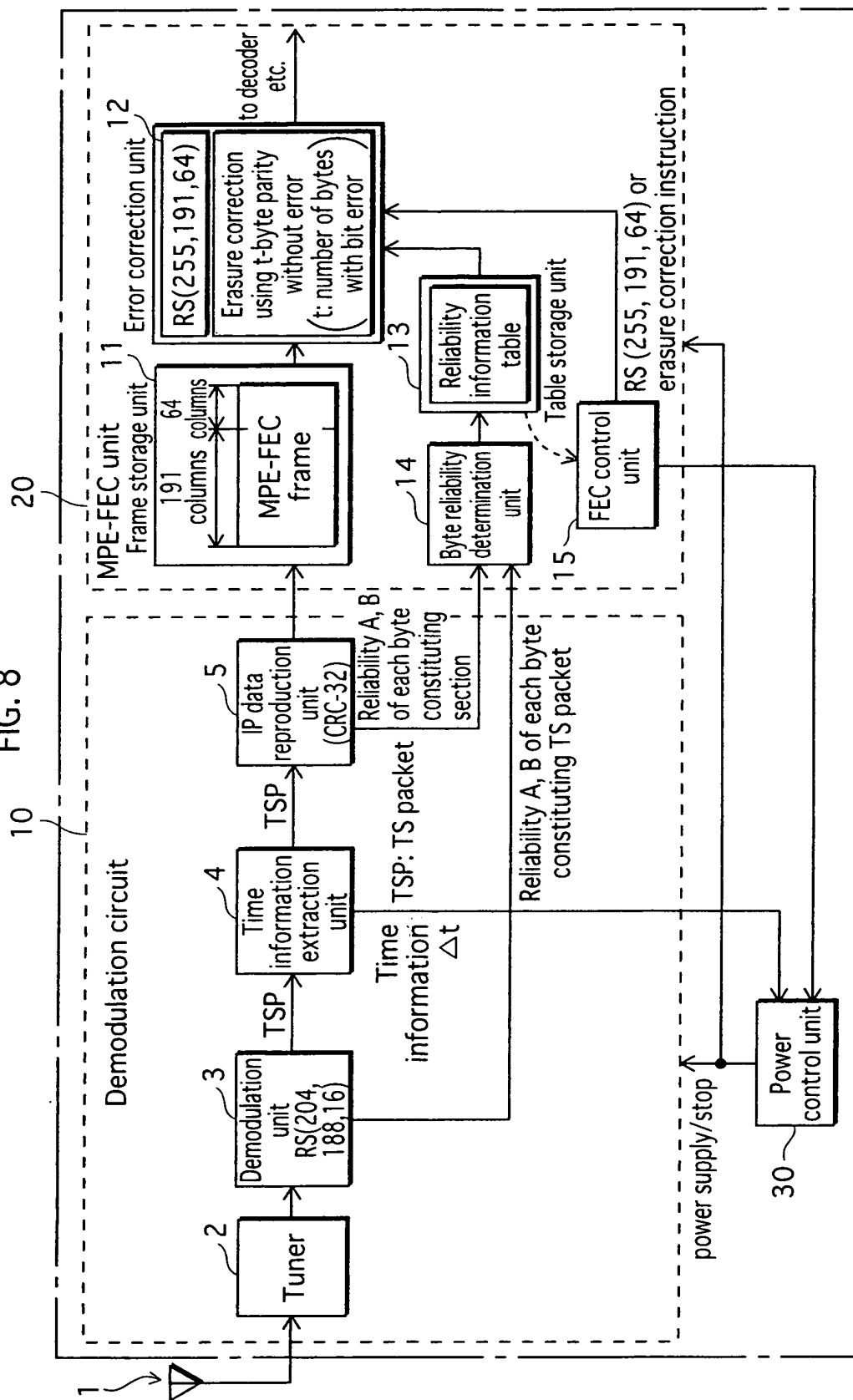


FIG. 9

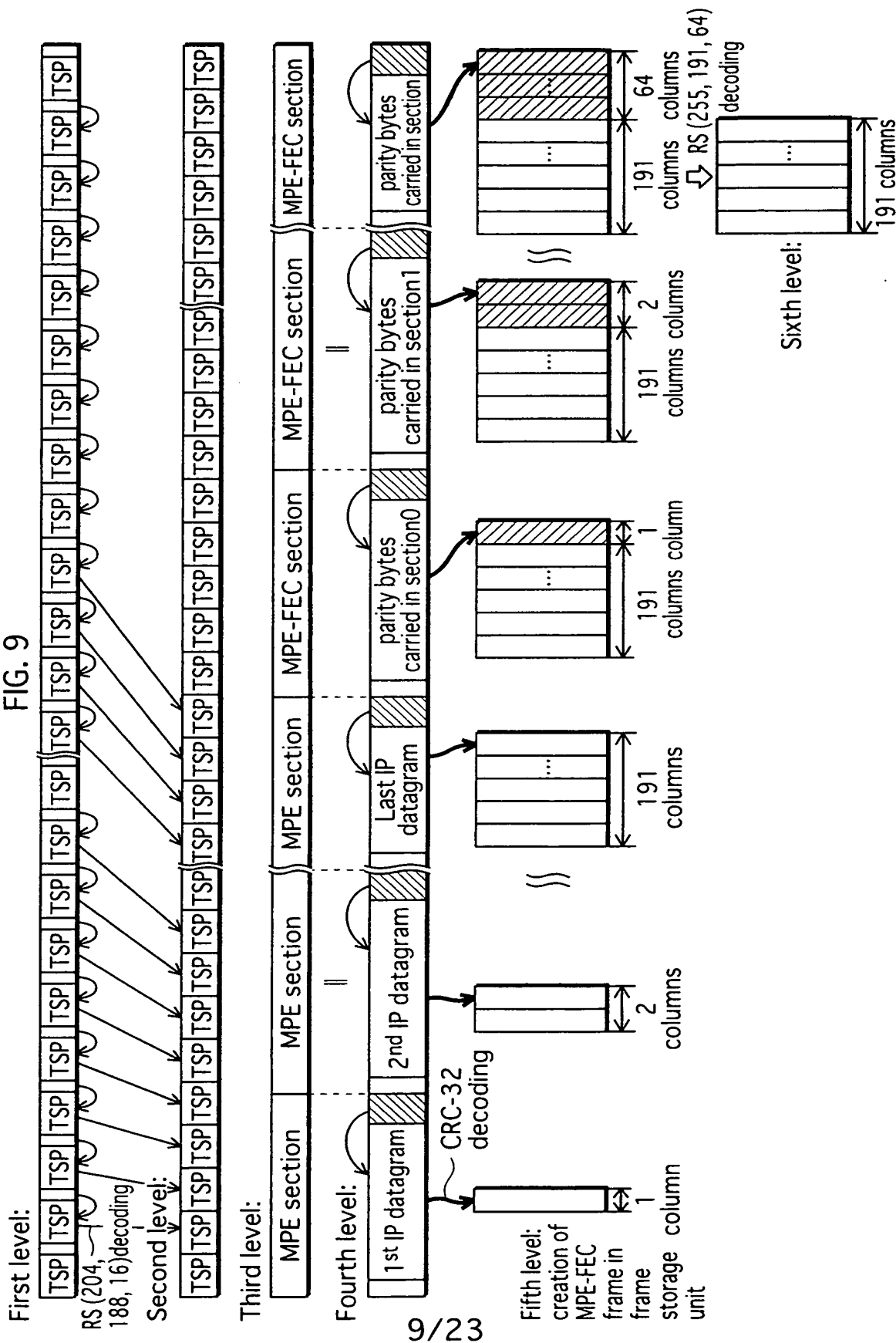


FIG. 10

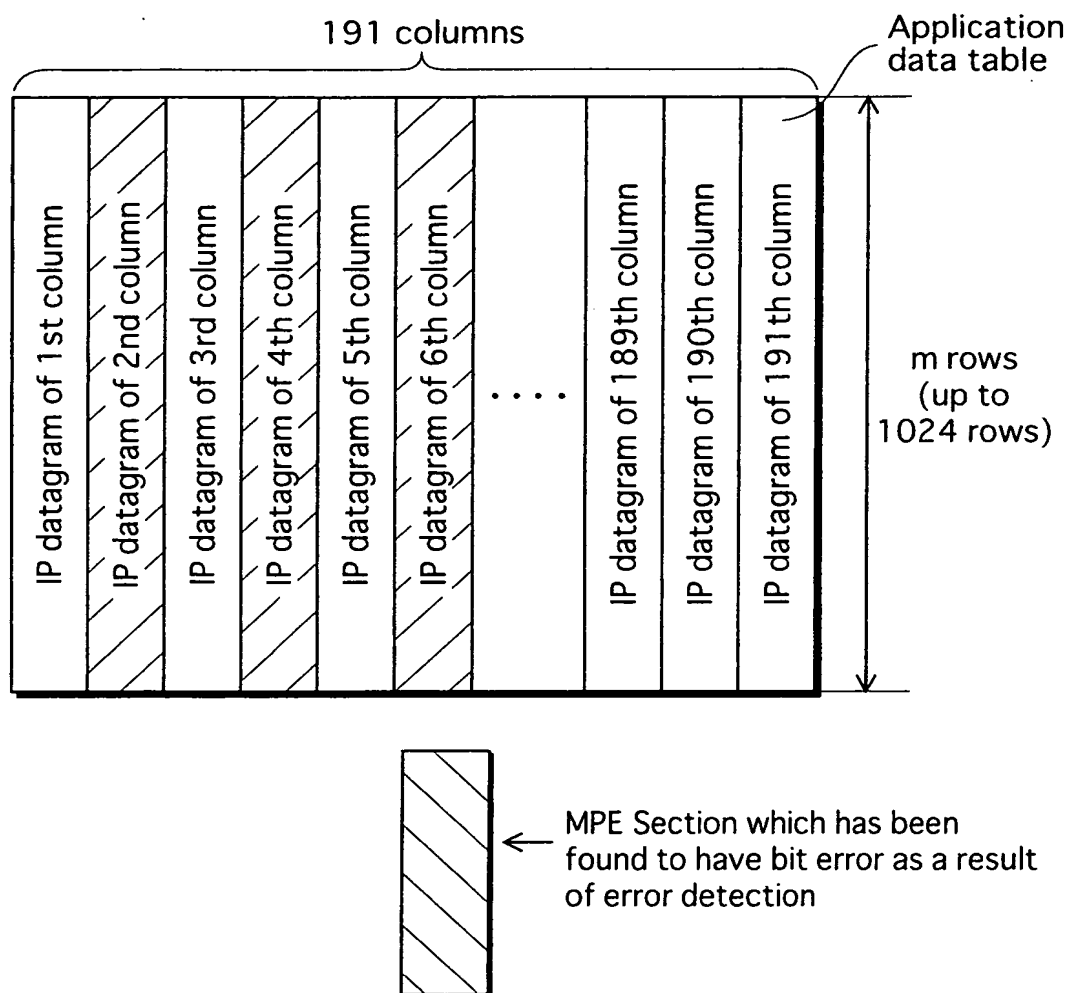


FIG. 11

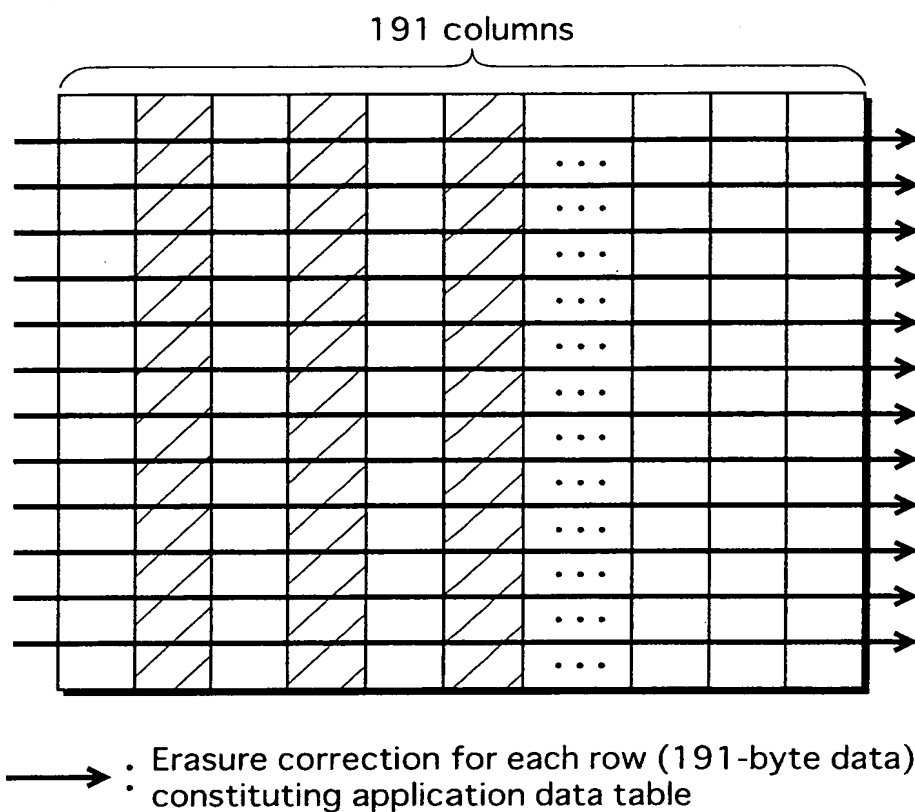
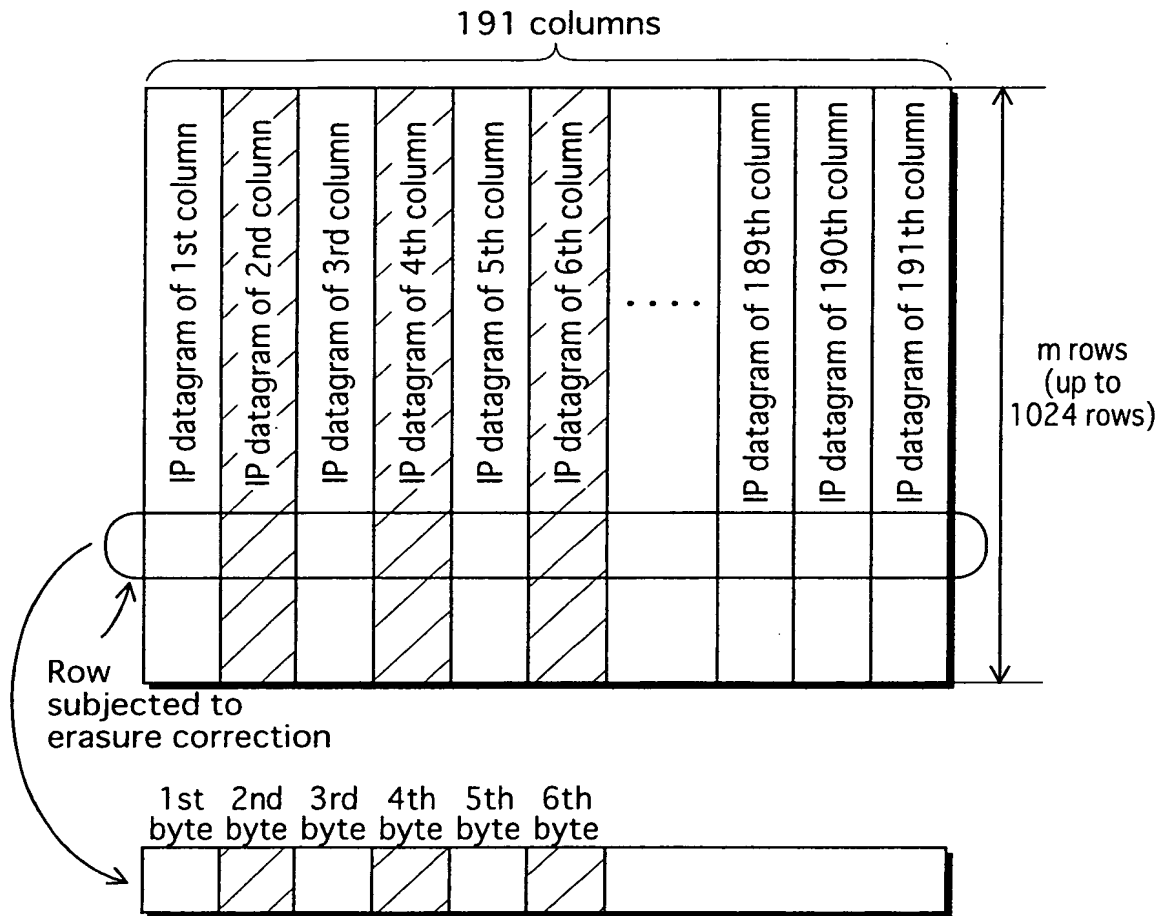


FIG. 12

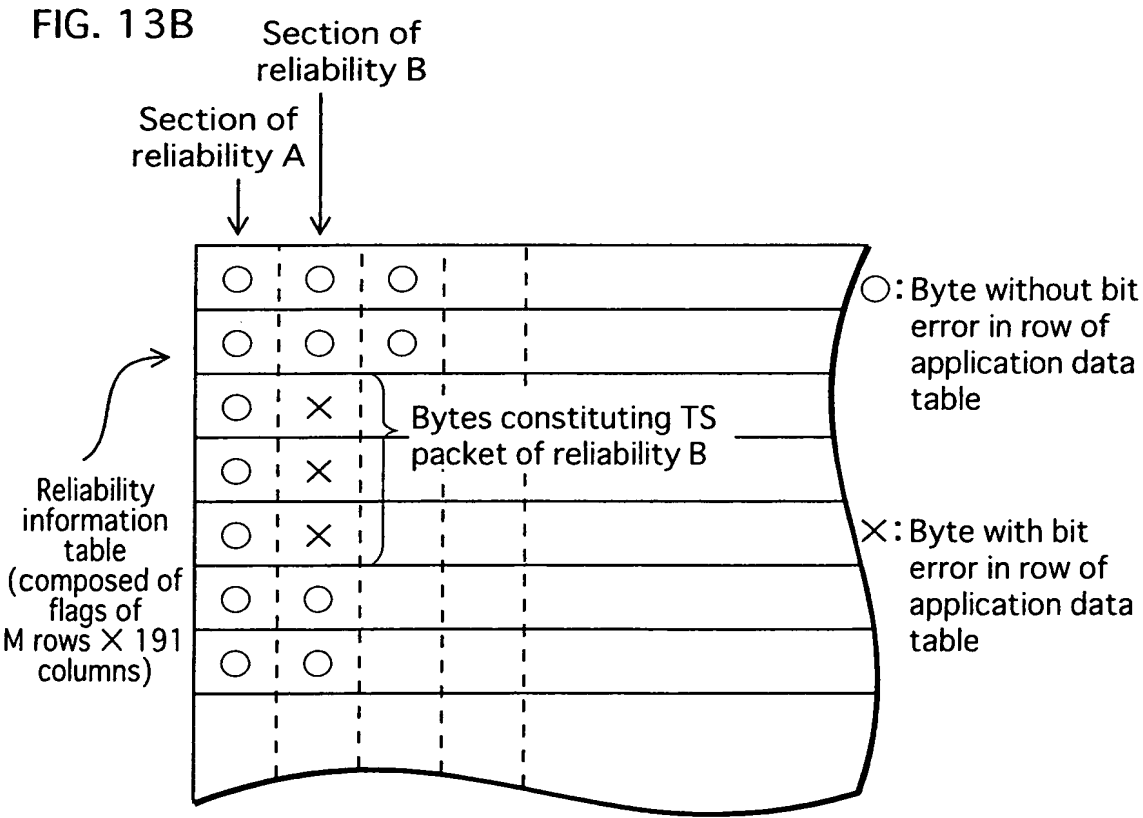


Byte positions having bit errors in 191-byte row are known
 → errors can be corrected by erasure correction if
 three pieces of parity data are available

FIG. 13A

		Result of CRC-32 for section	
		Result of CRC-32 = reliability A	Result of CRC-32 = reliability B
Result of RS (204, 188, 16) for TS packet in section	Entirely reliability A	Section reliability =A	Section reliability =A
	Partly reliability B	Section reliability =A	Section reliability =B

FIG. 13B



Switching to power saving mode
accelerated by this period

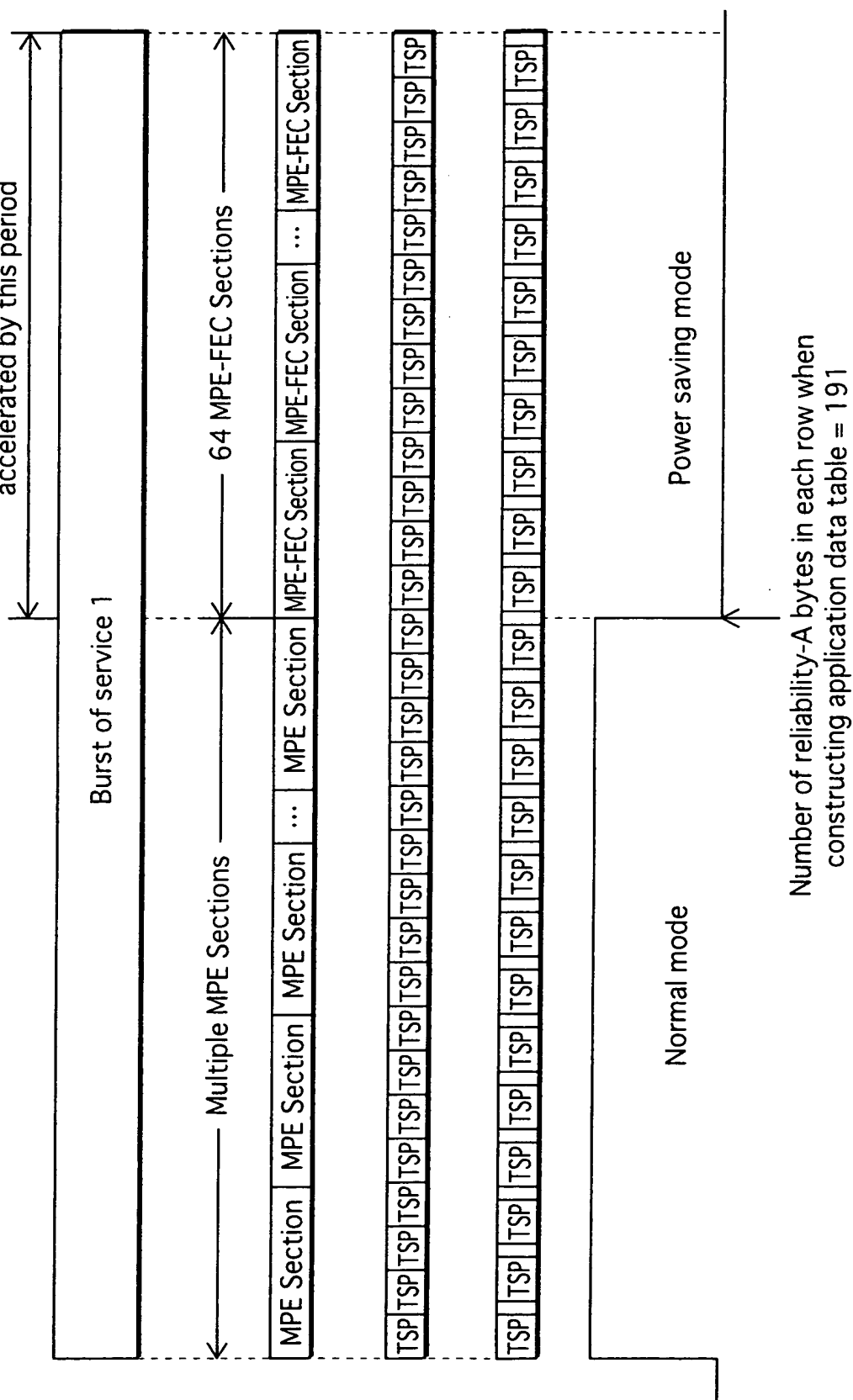


FIG. 17

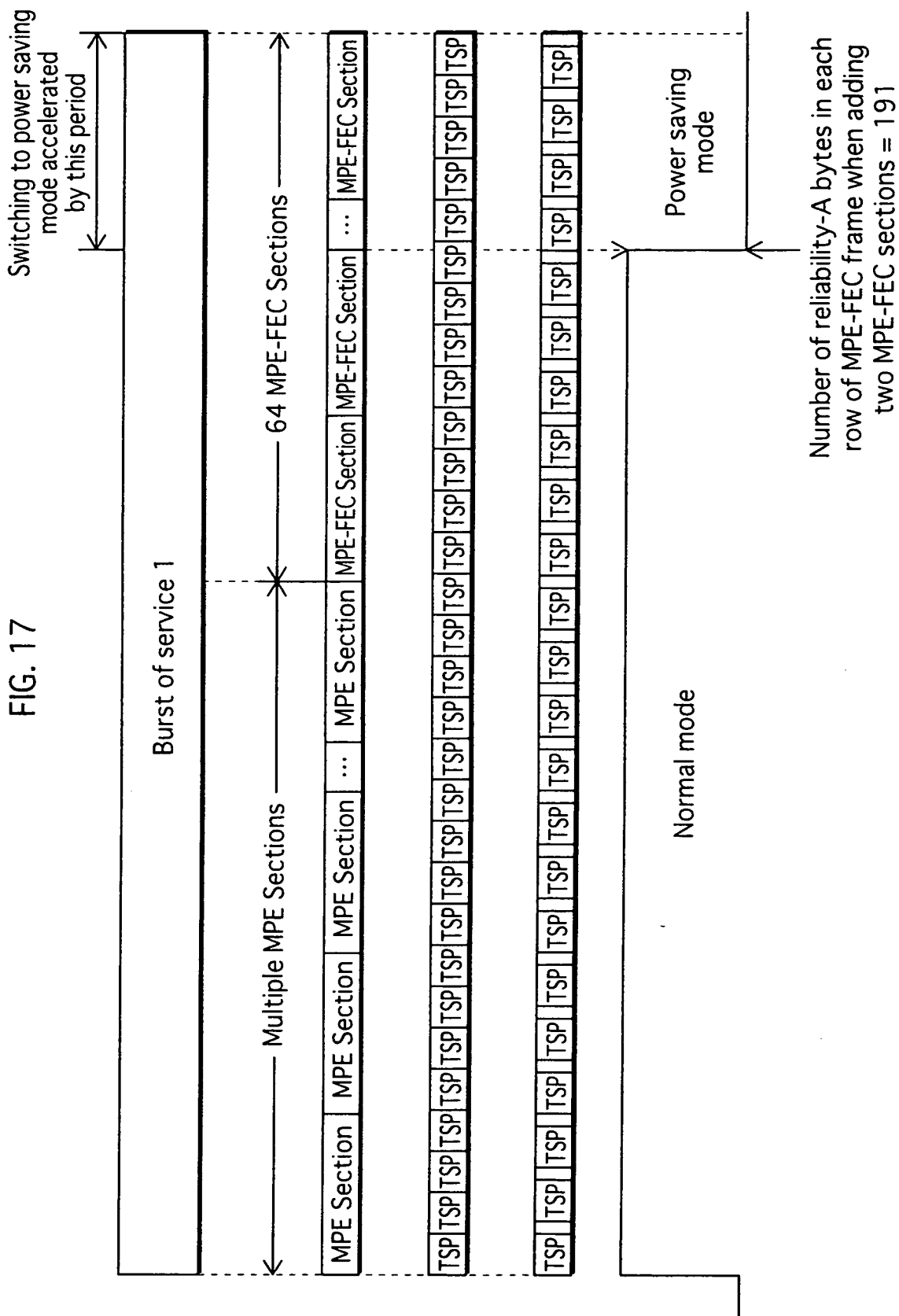


FIG. 18

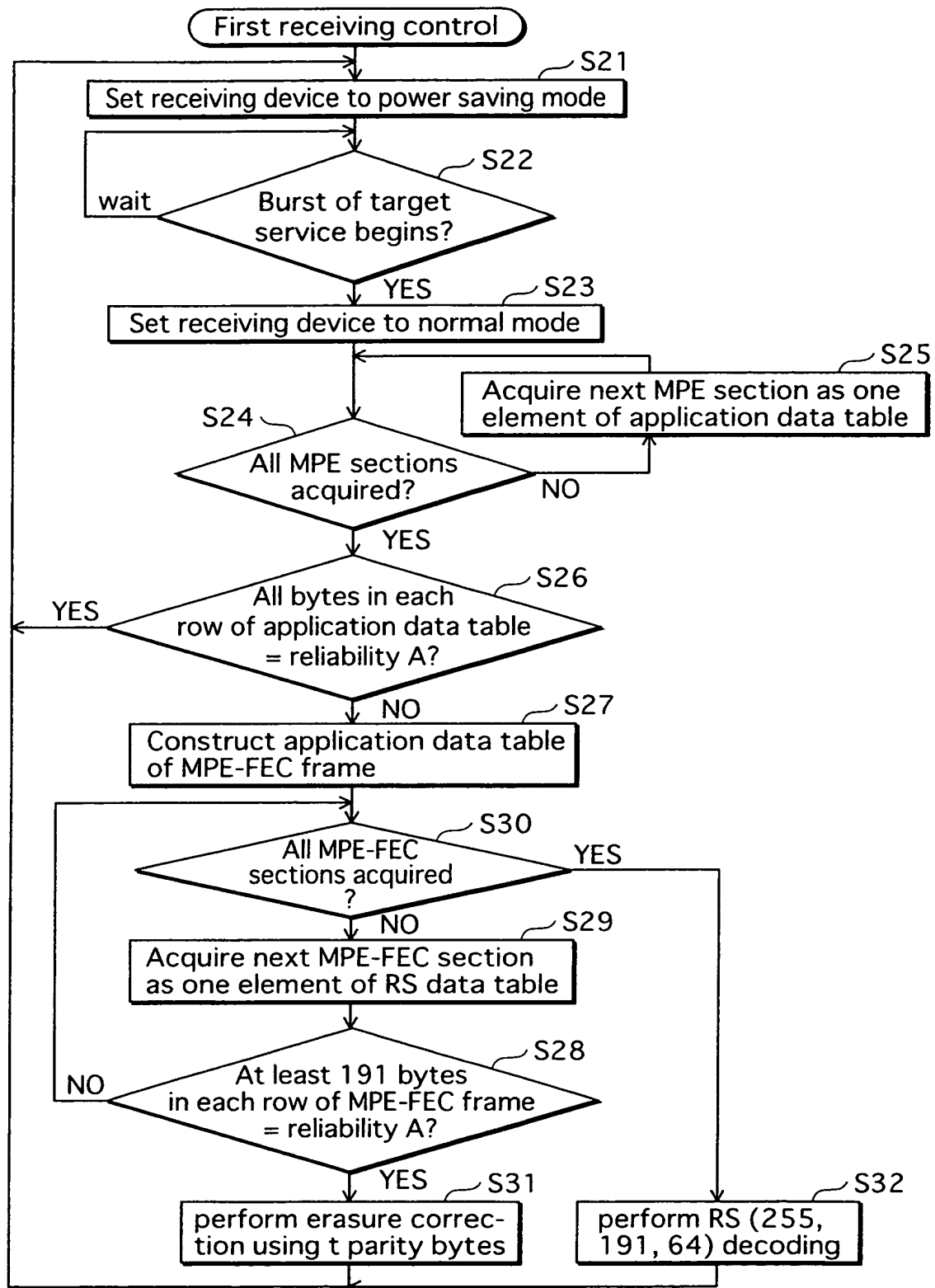


FIG. 19

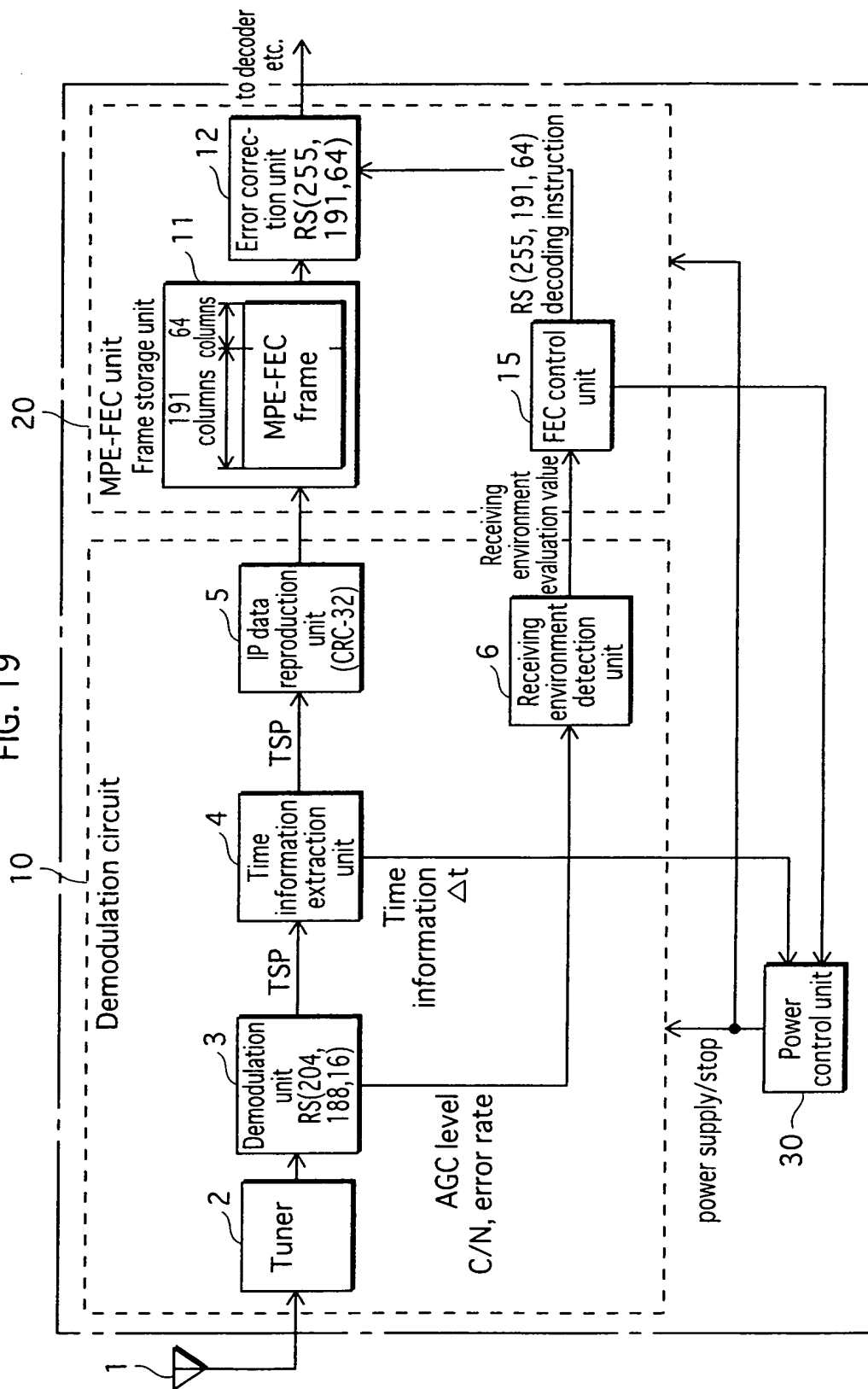


FIG. 20

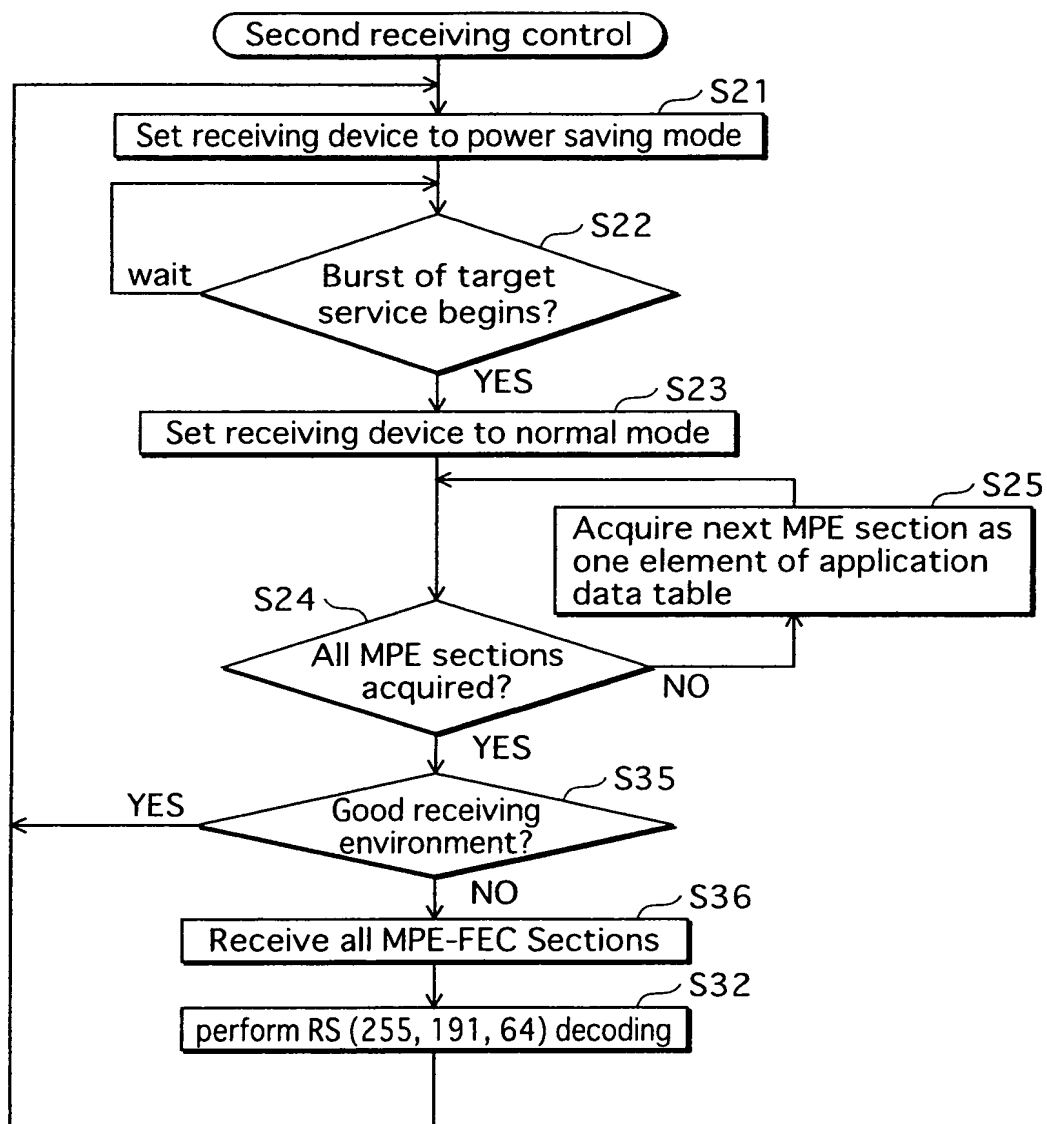


FIG. 21

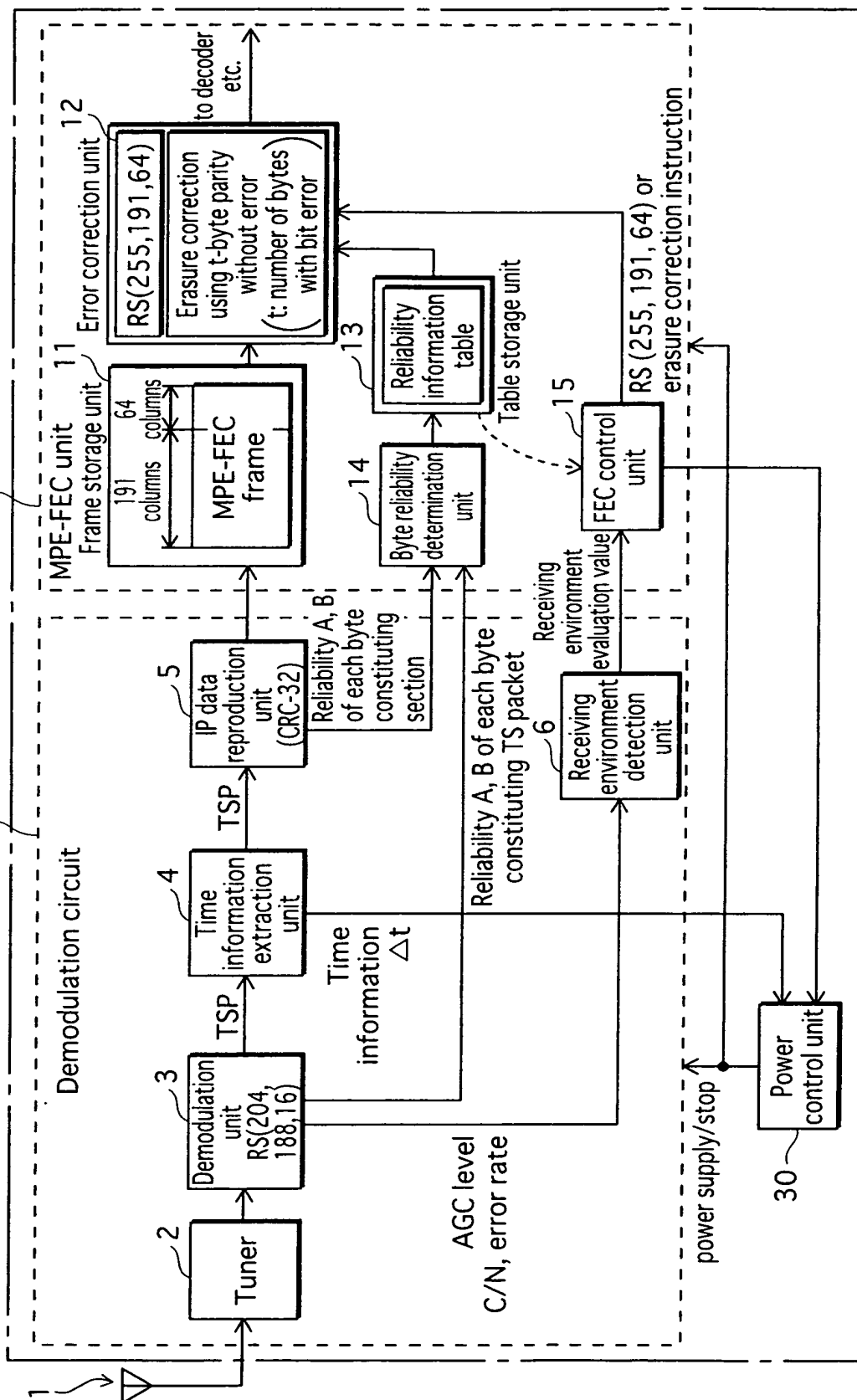


FIG. 22

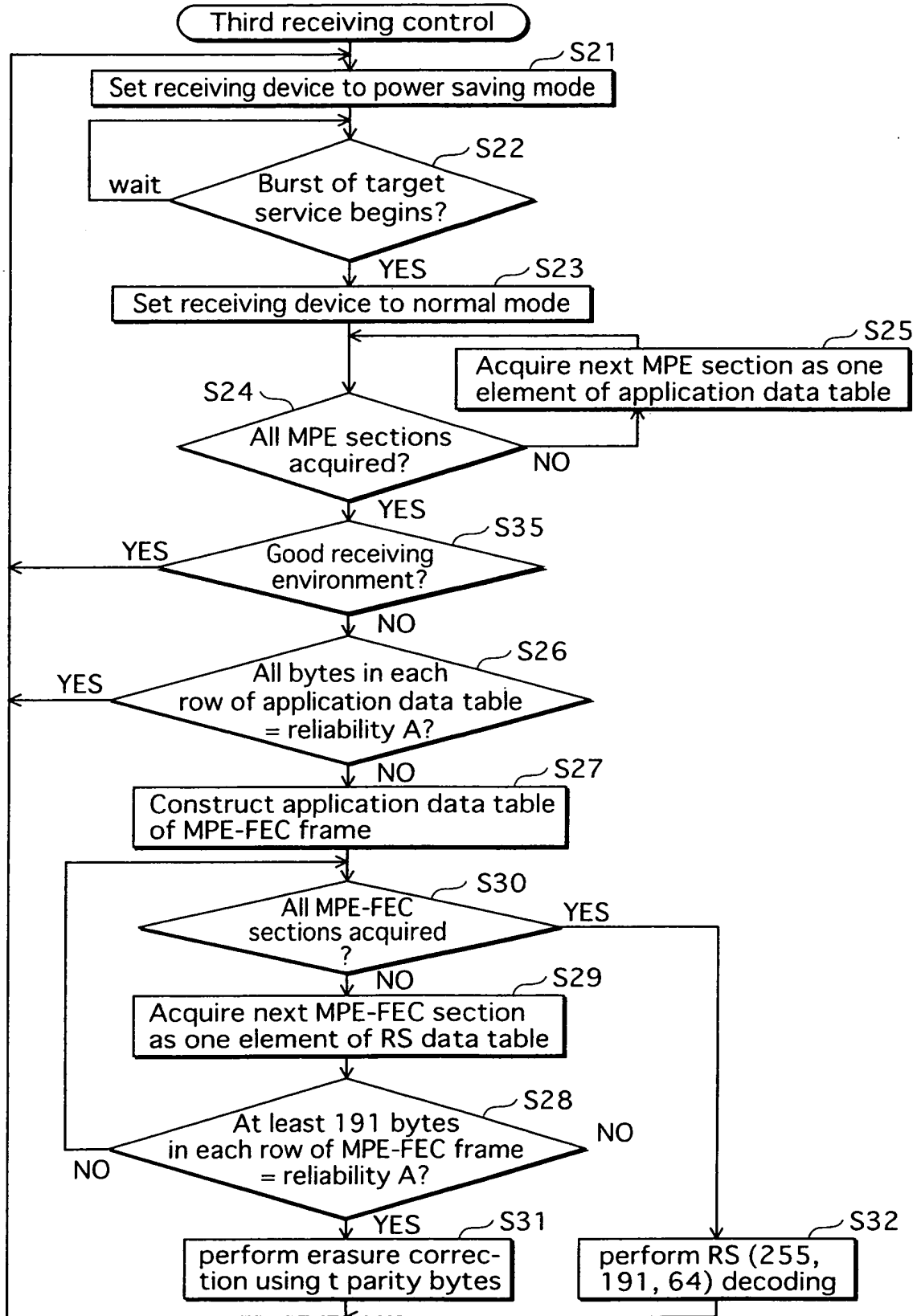


FIG. 23

